

Application S/N 10/643,327
Amendment Dated: August 7, 2006
Response to Office Action dated: March 10, 2006

CE11193J1210

REMARKS/ARGUMENTS

Claims 1-19 are pending in the application. In the Office Action, claims 1-3, 7-9, 11, 15, 18 and 19 were rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 6,912,716 to Johanson, et al. (Johanson) in view of U.S. Patent Application Publication No. 2002/0016899 to West, et al. (West). Also, claims 4-6, 12-14, 16 and 17 were rejected under 35 U.S.C. 103(a) as being unpatentable over Johanson in view of West and further in view of U.S. Patent No. 6,823,511 to McKenney, et al. (McKenney). Finally, claim 10 was rejected under 35 U.S.C. 103(a) as being unpatentable over Johanson in view of West and further in view of U.S. Patent No. 6,131,113 to Ellsworth (Ellsworth).

Independent claims 1, 11 and 15 have been amended to clarify that the first processor exclusively allocates the shared memory. Support for the amendment can be found on page 3, lines 7-9 and page 5, lines 7-8. No new matter has been added. In contrast, in Johanson, either processor can allocate portions of the shared memory (see col. 5, lines 31-43 and lines 58-63 and col. 6, lines 37-40). The apparatus and method as recited in the present claims permits one processor to control the allocation of the shared memory, which is easier to implement and is more amenable to scalability, such as when the number of processors sharing the memory increases beyond a mere two processors. Also, this feature is cheaper to implement, as the programming costs associated with it are minimized, as compared to a system where all the processors have the capability to allocate memory.

In addition, Applicants respectfully disagree with the Examiner's finding that West discloses the concept of the first processor sending a message buffer pointer to a

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second processor that directs the second processor to the message buffer. Specifically, West never mentions anything about processor-to-processor memory allocations; West merely describes intra-processor communications in which requesting software of a computer system requests and receives access to adaptor memory through system memory (see paragraphs 0029 and 0030).

As such, Applicants submit that independent claims 1, 11 and 15 are patentable over the prior art. Applicants also believe that those claims that depend from independent claims 1, 11 and 15 are patentable, both based on their dependencies on the independent claims and their patentability on their own. Reconsideration and withdrawal of the rejection of the claims is respectfully requested. Passing of this case is now believed to be in order, and a Notice of Allowance is earnestly solicited.

No amendment made was related to the statutory requirements of patentability unless expressly stated herein. No amendment made was for the purpose of narrowing the scope of any claim, unless Applicants have argued herein that such amendment was made to distinguish over a particular reference or combination of references.

In the event that the Examiner deems the present application non-allowable, it is requested that the Examiner telephone the Applicants' attorney or agent at the number indicated below so that the prosecution of the present case may be advanced by the clarification of any continuing rejection.

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The Commissioner is hereby authorized to charge any necessary fee, or credit any overpayment, to Motorola, Inc. Deposit Account No. 50-2117.

Respectfully submitted,

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